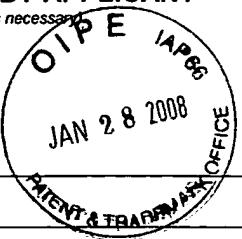


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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	10/643,577
Filing Date	August 18, 2003
First Named Inventor	Faanes, Gregory
Group Art Unit	2186
Examiner Name	Thomas, Shane

Attorney Docket No: 1376.711US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-20020169938A1	11/14/2002	Scott, S. L., et al.	12/14/2001
	US-20020172199A1	11/21/2002	Scott, S. L., et al.	12/14/2001
	US-20030005380A1	01/02/2003	Nguyen, H. T., et al.	06/29/2001
	US-20040044872A1	03/04/2004	Scott, S. L.	09/04/2002
	US-20040162949A1	08/19/2004	Scott, S. L., et al.	02/18/2003
	US-20050044128A1	02/24/2005	Scott, S. L., et al.	08/18/2003
	US-20070283127A1	12/06/2007	Kohn, J. R., et al.	06/29/2007
	US-4,541,046	09/10/1985	Nagashima, S. , et al.	03/23/1982
	US-4,888,679	12/19/1989	Fossum, Tryggve , et al.	01/11/1988
	US-5,197,130	03/23/1993	Chen, S. S., et al.	12/29/1989
	US-5,375,223	12/20/1994	Meyers, S. D., et al.	01/07/1993
	US-5,430,850	07/04/1995	Papadopoulos, G. M., et al.	07/22/1991
	US-5,430,884	07/04/1995	Beard, Douglas R., et al.	06/11/1990
	US-5,446,915	08/29/1995	Pierce, P. R.	05/25/1993
	US-5,530,933	06/25/1996	Frink, C. R., et al.	02/24/1994
	US-5,560,029	09/24/1996	Papadopoulos, G. M., et al.	05/31/1994
	US-5,649,141	07/15/1997	Yamazaki, T.	06/30/1995
	US-5,721,921	02/24/1998	Kessler, R. E., et al.	05/25/1995
	US-5,765,009	06/09/1998	Ishizaka, K.	01/08/1997
	US-5,787,494	07/28/1998	Delano, E. R., et al.	09/22/1995
	US-5,796,980	08/18/1998	Bowles, J. E.	02/22/1996
	US-5,835,951	11/10/1998	McMahan, S. C.	02/27/1996
	US-5,860,146	01/12/1999	Vishin, S. , et al.	06/25/1996
	US-5,897,664	04/27/1999	Nesheim, W. A., et al.	07/01/1996

EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		<i>Complete if Known</i>	
		Application Number	10/643,577
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		First Named Inventor	Faanes, Gregory
		Group Art Unit	2186
		Examiner Name	Thomas, Shane
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	US-5,987,571	11/16/1999	Shibata, M. , et al.	04/23/1997
	US-6,003,123	12/14/1999	Carter, N. P., et al.	02/10/1998
	US-6,014,728	01/11/2000	Baror, G.	01/21/1997
	US-6,088,701	07/11/2000	Whaley, K. M., et al.	11/14/1997
	US-6,101,590	08/08/2000	Hansen, C.	10/10/1995
	US-6,308,316	10/23/2001	Hashimoto, S. , et al.	01/08/1998
	US-6,490,671	12/03/2002	Frank, R. L., et al.	05/28/1999
	US-6,519,685	02/11/2003	Chang, S. S.	12/22/1999
	US-6,684,305	01/27/2004	Deneau, T. M.	04/24/2001
	US-6,782,468	08/24/2004	Nakazato, S.	12/13/1999
	US-6,922,766	07/26/2005	Scott, S. L.	09/04/2002
	US-6,925,547	08/02/2005	Scott, S. L., et al.	12/14/2001
	US-6,976,155	12/13/2005	Drysdale, T. G., et al.	06/12/2001
	US-7,243,211	07/10/2007	Kohn, J. R.	08/18/2003
	US-R,E28,577	10/21/1975	Schmidt, William G.	11/21/1973

FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
		ABTS, D , "So Many States, So Little Time: Verifying Memory Coherence in the Cray X1", <u>Parallel and Distributed Processing Symposium</u> , (April 22, 2003),11-20		
		CARLILE, BRADLEY R., "Algorithms and Design: The CRAY APP Shared-Memory System", <u>COMPCON Spring '93. Digest of Papers.</u> , (February 22, 1993),312-320		

EXAMINER

DATE CONSIDERED

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		<i>Complete if Known</i>	
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		CHEN, Y. , et al., "UTLB: A Mechanism for Address Translation on Network Interfaces", <u>Proceedings of the Eighth International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS)</u> , (1998),193-204	
		GHARACHORLOO, KOUROSH , "Two Techniques to Enhance the Performance of Memory Consistency Models", <u>Proceedings of the International Conference on Parallel Processing</u> , (1991),1-10	
		GUPTA, RAJIV , et al., "High Speed Synchronization of Processors Using Fuzzy Barriers", <u>International Journal of Parallel Programming</u> 19(1), (February, 1990),53-73	
		ISHIHATA, HIROAKI , et al., "Architecture of Highly Parallel AP1000 Computer", <u>Systems and Computers in Japan</u> , 24(7), (1993),69-76	
		O'KEEFE, MATTHEW T., et al., "Static Barrier MIMD: Architecture and Performance Analysis", <u>Journal of Parallel and Distributed Computing</u> , 25(2), (March 25, 1995),126-132	
		PATTERSON, DAVID A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA,(1996),39-41	
		PATTERSON, DAVID A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA,(1996),179-187, 373-384	
		PATTERSON, DAVID A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA,(1996),699-708	
		PATTERSON, D. A., et al., <u>Computer Architecture A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco,CA,(1996),255-260 & 308-317	
		PATTERSON, D. A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, San Francisco, CA,(1996),241-243	

EXAMINER
DATE CONSIDERED

Substitute for form 1449A/PTO		<i>Complete if Known</i>	
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		Group Art Unit	2186
		Examiner Name	Thomas, Shane
Sheet 4 of 4		Attorney Docket No: 1376.711US1	

		PATTERSON, D. , et al., <u>Computer Architecture: A Quantitative Approach</u> , Second Edition, Morgan Kaufmann Publishers Inc.,(1996),251-256	
		WOOD, D. A., et al., "An In-Cache Address Translation Mechanism", <u>Proceedings of the 13th Annual International Symposium on Computer Architecture</u> , (1986),358-365	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached